

Notice of References Cited	Application/Control No. 10/663,279	Applicant(s)/Patent Under Reexamination KHATAMI ET AL.	
	Examiner Kaushikkumar Patel	Art Unit 2188	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Designing for Upgrade to the 3 Volt Advanced+ Boot Block Flash Memory" by Intel, pub. Dec. 1998, pages 12.
	V	"8 Mbit / 16 Mbit (X8) Multi-Purpose Flash" by Silicon Storage Technology, Inc. pub, 06/2001, pages 4.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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